

ESSDERC-ESSCIRC 2009 CONFERENCE AT A GLANCE

Tuesday Sep 15th, 2009

	PELLA	MYCENAE	OLYMPIA	ILISSOS	MACEDONIA A	MACEDONIA B
08:40	OPENING ADDRESS (D. Tsoukalas NTUA) Room: OLYMPIA					
09:00	JOINT PLENARY 1 (T-C Chen, IBM T.J. Watson Research Center) Room: OLYMPIA					
09:50	More than Moore Devices (3 papers)	DRAM and OTP (3 papers)	New CMOS for Improved SRAM Circuits Robustness (Joint Session, 3 papers)	Wideband LNAs (3 papers)	Analog Signal Conditioning (4 papers)	
10:50	Coffee					
11:20	Thin Film 22 nm CMOS & Beyond (4 papers)	Non-CMOS Device Modeling (4 papers)	High Frequency Devices (4 papers)	RF Building Blocks (3 papers)	High-Voltage High-Current (4 papers)	Digital Circuits (4 papers)
12:40	Lunch					
14:10	JOINT PLENARY 2 (T. Shibata, University of Tokyo), Room: OLYMPIA					
15:00	ESSDERC PLENARY 1 (R. Waser, Aachen University), Room: OLYMPIA			ESSCIRC PLENARY 1 (W. Dehaene, Katholieke Universiteit Leuven), Room: ILISSOS		
15:40	Coffee					
16:10	Carbon Based Electronics (5 papers)	Alternative memories (5 papers)	Characterization of Dielectrics (5 papers)	RF Power Amplifiers (4 papers)	Integrated Wireless (3 papers)	3-D Imaging (4 papers)
18:00	PANEL DISCUSSION (Moderator: D. Antoniadis, MIT) Room: OLYMPIA					
19:30 20:00	Welcome Reception (Roof Garden of Conference Hotel)					

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Wednesday Sep 16th, 2009

	PELLA	MYCENAE	OLYMPIA	ILISSOS	MACEDONIA A	MACEDONIA B
08:40	BEST STUDENT PAPER AWARD CEREMONY (Room: OLYMPIA)					
09:00	JOINT PLENARY 3 (V. Zhimov, Semiconductor Research Corporation) Room: OLYMPIA					
09:50	Silicon Nano-Structures (3 papers)		Circuits For New Technologies (Joint Session, 3 papers)	Single Chip Wireless (3 papers)		Optical Sensors and Vision Systems (4 papers)
10:50	Coffee					
11:20	Flash Memories (4 papers)	Process & Thermal Modeling (4 papers)	Reliability & Degradation (4 papers)	UWB Circuits (3 papers)	Power Management Circuits (4 papers)	Circuits for MEMs (4 papers)
12:40	Lunch					
14:10	JOINT PLENARY 4 (M. Roukes, California Institute of Technology), Room: OLYMPIA					
15:00	ESSDERC PLENARY 2 (J. Appenzeller, Purdue University), Room: OLYMPIA			ESSCIRC PLENARY 2 (A. Hajimiri, California Institute of Technology), Room: ILISSOS		
15:40	Coffee					
16:10	Nanowire & Transport Engineering (4 papers)	RF MEMS (4 papers)	Characterization Techniques (4 papers)	RF & MM-Wave Transceiver Building Blocks (5 papers)	Oversampled ADCs (5 papers)	Processors & Memories (5 papers)
18:00	ESSDERC TPC Meeting (Room: OLYMPIA)			ESSCIRC TPC Meeting (Room: ILISSOS)		
19:00	Departure for Gala Dinner					
19:30	CONFERENCE BANQUET					

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Thursday Sep 17th, 2009

	PELLA	MYCENAE	OLYMPIA	ILISSOS	MACEDONIA A	MACEDONIA B
09:00	JOINT PLENARY 5 (C. Cognetti, ST Microelectronics) Room: OLYMPIA					
09:40	ESSDERC-ESSCIRC 2010 (Room: OLYMPIA)					
10:00	Process Modules (3 papers)	MEMS (3 papers)	Technology Impact On Circuit Design (Joint Session, 3 papers)		Wireline Communications (3 papers)	
11:00	Coffee					
11:20	High Mobility Channels & High K Dielectrics (4 papers)	Variability (4 papers)	CMOS Characterization (4 papers)	Frequency Generation (3 papers)	Digital Processing (4 papers)	Biomedical Applications (4 papers)
12:40	Lunch					
14:10	JOINT PLENARY 6 (J. Burghartz, IMS CHIPS), Room: OLYMPIA					
15:00	ESSDERC PLENARY 3 (S. Pantelides, Vanderbilt University), Room: OLYMPIA			ESSCIRC PLENARY 3 (K. Bult, Broadcom Corporation), Room: ILISSOS		
15:40	Coffee					
16:10	Tunneling Devices (5 papers)	Physics Based Models for CMOS & Post- CMOS Devices (4 papers)	RF Power Transistors (4 papers)	RF VCOs (4 papers)	DC-DC Converters (4 papers)	Nyquist-Rate ADCs (4 papers)
17:50						
18:00	CLOSING REMARKS (Room: OLYMPIA)					
18:15						